



DIN rail mounting, 6 inputs, 2 timers and 2 outputs digital I/O module



D8 line

User manual

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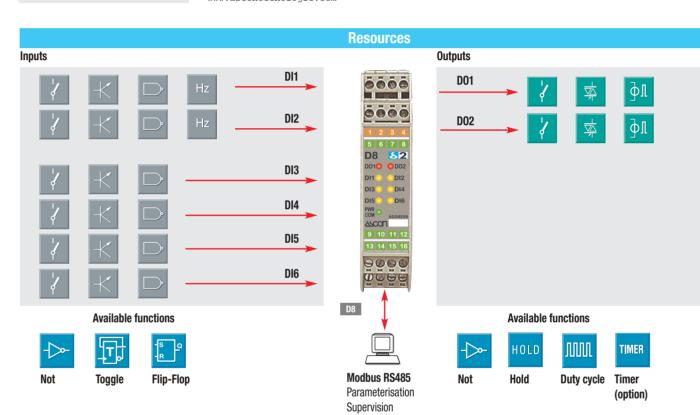
Ascon Tecnologic S.r.I.

viale Indipendenza 56, 27029 - Vigevano (PV) Tel.: +39 0381 69871, Fax: +39 0381 698730

www.ascontecnologic.com

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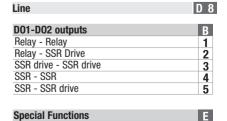


Model code

Configuration

> 0 2

The product code indicates the specific hardware configuration of the instrument, that can be modified by specialized engineers only.



None

2 Timers

| User manual | F |
|----------------------------|---|
| Italian/English (standard) | 0 |
| French/English | 1 |
| German/English | 2 |
| Spanish/English | 3 |

| Input type | |
|--------------------------------|---|
| No frequency input | 0 |
| Frequency input on DI1 | 1 |
| Frequency input on DI1 and DI2 | 2 |

| Output type | | L |
|---------------------------|-----|---|
| No PWM output | | 0 |
| PWM output on DO2 | [1] | 1 |
| PWM output on D01 and D02 | [2] | 2 |

Notes:

[1] Only when $\mathbf{B} = 2, 3, 4$ and 5; [2] Only when $\mathbf{B} = 3, 4$ and 5.

Configuration parameters description

Configuration parameters, shown in the table at page 3 have been divided into groups with homogeneous functionalities. The parameters described hereafter are in the same order as they are listed in the parameters table.

Configuration

Parameter

Parameter

PWM.f PWM frequency for both D01 and D02 - Table 1

| Parameter | Description | Value | Function |
|-----------|---------------|--------|----------------------------|
| PWM.f | PWM frequency | 010000 | 1 = 0,1 Hz, 10000 = 1 kHz |

Both outputs, when selected as SSR or SSR drive can be configured as PWM (Pulse Width Modulation) outputs with adjustable frequency and Duty Cycle. PWM frequency is applied to both DO1 and DO2 outputs (see also DC.O_ parameter).

t.F.I_ filter time for DI_ inputs - Table 2

| Parameter | Description | Value | Function |
|-----------|-------------------|-------|--|
| t.F.I_ | Input filter time | 0255 | Allows the user to set, separately for each input (DI_ = DI1 DI6), a filtering time with steps of 16 ms (0 = disabled, 1 = 16ms, 255 = 4080ms) |

In order to eliminate problems caused by disturbances, the user can define the minimum signal width (input time). The input filter is made by units of 16ms (1... 255 units). Value 0 disables the input filter.

Function

Nt.I_ Negate (NOT) input status - Table 3

Value

Value

Description

| Nt.I_ | NOT (negate) DI_ | 0 1 |
|-----------------------------|------------------|--------|
| Input status | On Off | + |
| Enable NOT function | On Off | + |
| Internal logic status | On Off | 1 |

Negates input status (DI_ = DI1... DI6)

Is possible to enable, separately for each input (DI1...DI6), the negate (NOT) fuction of the physical input status in order to operate in inverted logic.

Disables negate input status (DI_ = DI1... DI6)

Note: The NOT command influences also the status of the Toggle (TG.I_) and Flip-Flop (FF.I_) functions (see "Logic function diagram" at page 3).

Nt.O_ Negate (NOT) output status - Table 4

| Nt.O_ | NOT (negate) DO_ 1 |
|-----------------------------|--------------------|
| Internal logic status | On Off |
| Enable NOT function | On Off |
| Output status | On Off |

Description

Negates output status (D0_ = D01, D02)

It is possible to enable, separately for each output (D01and D02), the negate (N0T) fuction of the output internal logic status.

Disables negate output status (D0_ = D01, D02)

Parameters description

Parameters, shown in the table at page 3 have been divided into groups with homogeneous functionalities. The parameters described hereafter are in the same order as they are listed in the parameters table.

Parameters

DO.O Output status at power ON - Table 5

| Parameter | Description | Value | Function |
|---------------------|-----------------|---|--|
| DO.0 DO at power ON | 0 | Output OFF at power ON (DO_ = DO1, DO2) | |
| 00.0_ | DO_ at power on | 1 | Output ON at power ON (DO_ = DO1, DO2) |

It allows to specify, separately for each output (D01, D02), which status the physical output must assume at power ON independently from the last written value.

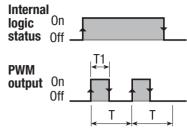
DT.O_ Duty Cycle at power ON - Table 6

| | _ | | |
|-----------|-----------------|-------|------------------------|
| Parameter | Description | Value | Function |
| DT.O_ | DC_ at power ON | 01000 | Duty Cycle at power ON |

It allows to specify, separately for each output (D01, D02), the Duty Cycle (ON output period expressed as a percentage of the total PWM period) the module must use at power ON, independently from the last value written in the DC.O_ parameters.

DC.O_ DO_ output Duty Cycle of PWM - Table 7

| Parameter | Description | Value | Function |
|-----------|-----------------------|--------|--|
| DC.0_ | DO_ output Duty Cycle | 0 1000 | It set the time an output stays ON as a percentage of the total PWM period (DO_ = DO1 and DO2) |
| Internal | | | When the SSR or SSR drive outputs have |



When the SSR or SSR drive outputs have been selected as PWM output with a specific frequency, the Duty Cycle value of each output (ON output period expressed as a percentage of the PWM period) can be specified. The Duty Cycle of each output can be selected through the parameter DC.O_ which allows the following values:

 $0...\ 1000\ (0 = 0\%;\ 1000 = 100\%).$

T = PWM period [s]

PWM frequency = $\frac{1}{T}$ [Hz]

Duty cycle = $\frac{T1}{T}$ x 100 [%]

TG.I_ DI_ Toggle - Table 8

| I G.I_ | DI_ loggie - lable o | | |
|--------------------------------|----------------------|-------|--|
| Parameter | Description | Value | Function |
| TG.I_ | DI_ input Toggle | | Manages an internal logic status connected to the inputs as a bistable type T register (DI_ = DI1DI6) |
| Input status | On Off | | Bistable that changes its status at the leading edge of the input signal. An internal logic status (T) is connected to each input. The internal logic status value changes every time the input sta- |
| Internal logic status (1 | On Off | | tus changes from 0 to 1. |

FF.I DI Flip-Flop - Table 9

| _ | | | |
|-----------|---------------------|-------|--|
| Parameter | Description | Value | Function |
| FF.I_ | DI_ input Flip-Flop | | Manages an internal logic status connected to the inputs as a bistable type SR register (DI_ = DI1DI6) |



| Input | | Internal logic | | | |
|-------|---|----------------|--|--|--|
| S | R | status (SŘ) | | | |
| 0 | 0 | Unchanged | | | |
| 0 | 1 | 0 | | | |
| 1 | 0 | 1 | | | |
| 1 | 1 | 0 | | | |

Bistable (type SR); it changes the value of the register Q to 1 at the leading edge of the SET input signal and changes the value register Q to 0 at the leading edge of the RESET input signal. SET and RESET inputs are couples of phisycal inputs that cannot be changed (DI1 and DI2, DI3 and DI4, DI5 and DI6).

DI1, DI3 and DI5 are SET inputs, DI2, DI4 and DI6 are RESET inputs.

HL.O_ DO_ HOLD- Table 10

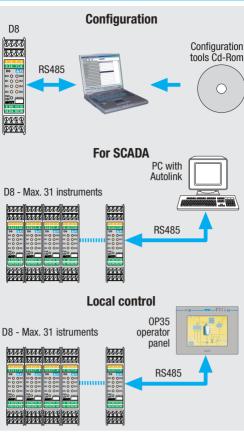
| HL.O_ | DU_ HULD- Table TO | | | | | |
|-----------------------------|--------------------|----------|--|--|--|--|
| Parameter | Description | Value | Function | | | |
| HL.0 | HOLD output DO | 0 | Releases the output status (DO_ = DO1, DO2) | | | |
| IIL.U_ | HOLD output DO_ | 1 | "Freezes" the output status (DO_ = DO1, DO2) | | | |
| Internal logic status | On Off | · | The HOLD output command freezes the status of a phisycal output (D01 or D02) to the actual value. As long as the HOLD command is active if the integral logic status changes | | | |
| Enable HOLD signal | On Off | <u> </u> | is active, if the internal logic status changes the corresponding output status does no change. | | | |
| Output | _ | 1 | | | | |

Addr Communications address

status

This parameter can be set between 1...247 and must be unique (no other instrument connected to a supervisor can have the same number).

Serial communications connection example



Communications parameters reset

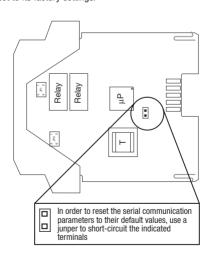
The serial communications parameters can be reset to the original factory settings (protocol: Modbus, Baud Rate: 9600, Address: 247).

The instructions to remove/re-insert the I/O module from/in its plastic case are described in the "Installation manual".

After having removed the I/O module, use the instructions that follow to reset the communications parameters:

- 1) Use a jumper to short-circuit the terminals shown in the drawing that follows;
- 2) Insert the I/O module in its housing and power ON the instrument;
- 3) Power OFF the instrument, extract the I/O module from its plastic case and remove the short circuit jumper;
- 4) Reinstall the module in its housing.

At the end to this procedure, the communications parameters will be reset to its factory settings.



Warranty

We warrant that the products will be free from defects in material and workmanship for 3 years from the date of delivery.

The warranty above shall not apply for any failure caused by the use of the product not in line with the instructions reported on this manual.

Table of standard parameters

Configuration parameters

| Mnemonic code | Parameter description | Range | Unit | Factory setting | Notes |
|---------------|-------------------------|---------------------------|---------------|-----------------|-----------------------|
| PWM.f | PWM frequency | 010000 | 1/10 of Hz | 0 | See table 1 at page 2 |
| t.F.I_ | Input filter time | 1255 | Steps of 16ms | 0 | See table 2 at page 2 |
| baud | Baud rate | 1200, 2400, 4800, 9600 | Baud | 9600 | |
| Nt.I_ | Negate (NOT) DI_ | 0/1 | | 0 | See table 3 at page 2 |
| Nt.O_ | Negate (NOT) DO_ | 0/1 | | 0 | See table 4 at page 2 |
| Prot | Communications protocol | M.bus/Jbus | | M.bus | |

Parameters

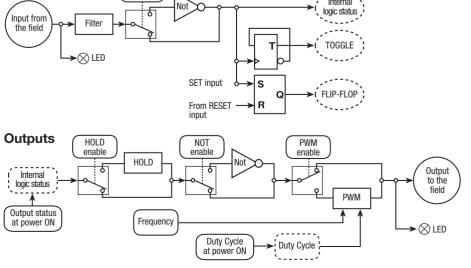
| Mnemonic code | Parameter description | Range | Unit | Factory setting | Notes |
|---------------|------------------------------|-------|---------------|-----------------|------------------------|
| D0.0_ | Output status at power ON | 0, 1 | | 0 | See table 5 at page 2 |
| DT.O_ | Duty cycle DO_ at power ON | 01000 | Steps of 0.1% | 0 | See table 6 at page 2 |
| DC.O_ | Duty Cycle output DO_ | 01000 | Steps of 0.1% | 0 | See table 7 at page 2 |
| TG.I_ | Toggle | 0, 1 | | | See table 8 at page 2 |
| FE.I_ | Flip-Flop | 0, 1 | | | See table 9 at page 2 |
| HL.O_ | Hold DO_ output | 0, 1 | | 0 | See table 10 at page 2 |
| Addr | Serial communication address | 1247 | | 247 | |

Technical specification

| | • | | | | | |
|-------------------------------------|--|---|-----------------------------|--|--|--|
| Features (at 25°C env. temperature) | Description | | | | | |
| Total configurability | Using the configuration tools on a PC it is possible to choose: - Input type - Functions to elaborate inputs/outputs - Output type | | | | | |
| | 6 digital inputs in 2 groups (DI1, DI2 and DI3, DI4, DI5, DI6), optoisolated | | | | | |
| Inputs | Voltage inputs | Compatible with EN61131-2 standard: - logic status voltage 1 ≥ 5Vdc; - logic status voltage 0 ≤ 2Vdc; - max. voltage allowed: 24Vdc | | | | |
| | Frequency inputs (DI1 and DI2) | 0.110 kHz | | | | |
| | No voltage inputs | Minimum pulse width: 16 ms (table 2) | | | | |
| Outputs | SPST relay contact: SSR: SSR drive: | 2A/250Vac (4A/120Vac) for resistive loads; 1A/250Vac for resistive loads; 5Vdc ±20%, max. 30mA | | | | |
| Serial communications | Isolated RS485, protocol Modbus/Jbus, 1200, 2400, 4800, 9600 bit/s, 2 wires | | | | | |
| Operational safety | Parameters | Parameters and configuration data are stored in a non volatile memory for an unlimited time | | | | |
| | Power supply (PTC protected) | 24Vac (-25+12%) 50/60Hz and 24Vdc (-15+25%) | Power consumption 3.2W max. | | | |
| | Safety | EN61010-1 (IEC1010-1) installation class 2 (2.5kV), pollution degree 2, instrument class II | | | | |
| General characteristics | Electromagnetic compatibility | Compliance to CE standards | | | | |
| | Protection | Terminal block: IP20 | | | | |
| | UL and cUL approval | File E176452 | | | | |
| | Dimensions | Pitch: 22.5 mm - depth: 114.5 mm | | | | |

Logic-function diagrams

Internal



NOT enable

Inputs

Timer

Description

Up to 2 timers can be activated in the instrument. The term "timer" for this instrument means all the functionalities connected to Digital Inputs, Digitl outputs and internal variables conditioned by a free-running counter that has a settable time base.

Timer types

Four types of timer can be set:

- Free Run;
- Pulse;
- · Countdown;
- · Countdown Latched.

The following parameters must be doubled: one set for Timer1 and 1 set for Timer2.

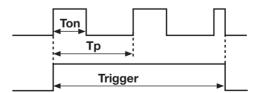
| | Configuration | | | | | | |
|---------------|---|---|--------------|-----------------|---|--|--|
| Mnemonic code | Parameter description | Range | Unit | Factory setting | Notes | | |
| TbTp | Time base for the Period (Tp) | 0, 1, 2 | S, Min, Hour | 0 | | | |
| Tb0n | Time base for T0n | 0, 1, 2 | S, Min, Hour | 0 | | | |
| Тр | Period value | 1 32000 | | 0 | The values must be referred to the | | |
| T0n | ON time value | 1 32000 | | 0 | respective time base | | |
| TTm | Timer Type | None; FreeRun; Pulse; Countdown; Countdown Latched | | None | | | |
| Trig | Digital Input associated to the Trigger | None; DI1, DI2 | | None | he Timer is active when the selected Digital Input is enabled (TEn = ON). If no Digital Input is selected (None), the timer is active depending on the Trigger status in the instrument memory (TrRa). | | |
| Rst | Digital Input associated to the Reset | None; DI1, DI2 | | None | | | |
| Out | Digital Output associated to the Timer | None; D01, D02 | | None | More than one funtion can be assigned to the same physical output. The Output status is the OR of the sources | | |
| TEnP | Timer enable value at Power ON | 0,1 | | 0 | | | |

Parameters

| Mnemonic code | Parameter description | Range | Unit | Factory setting | Notes |
|---------------|-----------------------|-----------------|------|-----------------|--|
| TEn | Timer enable | 0, 1 | | - 11 | At Start-Up this parameter is initialized to the TEnP value |
| TrRa | Trigger in memory | 0, 1 | | - 11 | For "Free Run" only, at Start-Up this parameter is initialized to the TEnP value |
| TRes | Memory Reset | 0, 1 | | 0 | |
| TEv | Events | Read only: 0, 1 | | | |

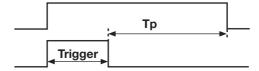
Free Run Timer

The user must define the Time period (Tp) and the duration of the periodic event (TOn). With Timer enabled (TEn = ON) and Trigger = ON the instrument generates the Event having a duration = TOn and periodicity = Tp, in continuous mode. Reset = ON pauses the Event generation.



Pulse timer

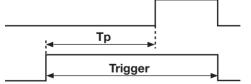
With Timer enabled (TEn = ON) and Trigger = ON the instrument generates the **Event** having a duration = Tp starting at Trigger = OFF. **Reset** = ON pauses the **Event** generation.



Operating modes

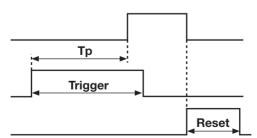
CountDown Timer

With Timer enabled (**TEn** = 0N) the instrument generates the **Event** after a delay having a duration = **Tp** starting at **Trigger** = 0N. The **Event** is maintained until the **Trigger** value goes 0FF. **Reset** = 0N pauses the **Event** generation.



CountDown Latched Timer

With Timer enabled (**TEn** = 0N) the instrument generates the **Event** after a delay having a duration = **Tp** starting at **Trigger** = 0N. The **Event** is maintained until the **Reset** value goes 0N (once the event is activated is independent from **TEn** signal); **Reset** = 0N pauses the **Event** generation.



To activate the **Evento**, **Trigger** must be active (ON) for a time longer than **TP**.